	Application No.		Applicant(s)	
Notice of Allowability	09/517,541 Examiner		SILVEDREDON ET AL	
			SILVERBROOK ET AL. Art Unit	
•				
	Nga B.	Nguyen	3692	••
The MAILING DATE of this communication apperature of the communication apperature of the communication apperature of the communication apperature of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REM) or other I <b>GHTS</b> .	MAINS) CLOSED in thi appropriate communic This application is subj	is application. If not inclusation will be mailed in du	uded ue course. THIS
1. $\boxtimes$ This communication is responsive to <u>the Amendment filed</u>	on Nove	<i>mber 22, 2006</i> .		
2. The allowed claim(s) is/are <u>1-10</u> .				•
3. Acknowledgment is made of a claim for foreign priority una) All b) Some* c) None of the:			r).	
Certified copies of the priority documents have				
2. Certified copies of the priority documents have				
<ol><li>Copies of the certified copies of the priority do</li></ol>	cuments	have been received in	this national stage appli	cation from the
International Bureau (PCT Rule 17.2(a)).		•		
* Certified copies not received:			·	
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this co MENT of t	mmunication to file a r	eply complying with the	requirements
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give	itted. Not es reasor	te the attached EXAMI n(s) why the oath or de	NER'S AMENDMENT or claration is deficient.	NOTICE OF
5. CORRECTED DRAWINGS (as "replacement sheets") mus	st be subr	mitted.		
(a) ☐ including changes required by the Notice of Draftspers			PTO-948) attached	
1)  hereto or 2)  to Paper No./Mail Date			, , , , , , , , , , , , , , , , , , , ,	
(b) ☐ including changes required by the attached Examiner' Paper No./Mail Date	•	ment / Comment or in t	the Office action of	
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) sh he header	ould be written on the d	rawings in the front (not t	the back) of
6. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT	sit of BIC FOR THE	OLOGICAL MATERI E DEPOSIT OF BIOLO	AL must be submitted GICAL MATERIAL.	. Note the
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)		5. Notice of Inform	nal Patent Application	
2. Notice of Draftperson's Patent Drawing Review (PTO-948)		6. Interview Sumr	nary (PTO-413),	•
3. Information Disclosure Statements (PTO/SB/08),		Paper No./Mai 7. Examiner's Am	il Date	
Paper No./Mail Date  4. Examiner's Comment Regarding Requirement for Deposit of Biological Material		8. X Examiner's Sta	tement of Reasons for A	llowance
o. Diological Material		9. 🗌 Other		
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## **DETAILED ACTION**

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1. This Office Action is the answer to the Amendment filed on November 22, 2006, which paper has been placed of record in the file.

2. Claims 1-10 are pending in this application.

## Allowable Subject Matter/Reasons for Allowance

3. Claims 1 and 5 are allowed over the prior arts cited records.

The closets prior art is:

1) Park (US 5,673,223) discloses in an authentication chip in which secret data is manipulated, a method of shielding manipulations of the secret data from observation, including the steps of: operating non-flashing CMOS structures in the chip.

Therefore, it is clear from the description of Park's invention that the prior arts do not considered the possibility of: operating non-flashing CMOS structures in the chip, in which pMOS and nMOS transistors are driven such that they do not have intermediate resistance simultaneously during a change of state of the CMOS structure, to manipulate the secret data and operating conventional CMOS inverters adjacent the non-flashing CMOS structures at the same time, as included in claims 1, non-flashing CMOS structures, in which pMOS and nMOS transistors are driven such that they do not have intermediate resistance simultaneously during a change of state of the CMOS structure, to manipulate the secret data and conventional CMOS inverters adjacent the non-flashing CMOS structures, to change stat at the same time the non-flashing CMOS structure manipulate secret data, as included in claim 5.

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4. Claims (2-4, 9) and (6-8, 10), are allowed because they are dependent claims of

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the allowable independents 1 and 5 above, in that order.

Conclusion

5. Claims **1-10** are allowable.

6. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to examiner Nga B. Nguyen whose telephone number is

(571) 272-6796. The examiner can normally be reached on Monday-Thursday from

9:00AM-6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Richard E. Chilcot can be reached on (571) 272-6777.

Any inquiry of a general nature or relating to the status of this application or

proceeding should be directed to the Group receptionist whose telephone number is

(571) 272-3600.

7. Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

C/o Technology Center 3600

Washington, DC 20231

Or faxed to:

(703) 872-9306 (for formal communication intended for entry),

or

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(571) 273-0325 (for informal or draft communication, please label "PROPOSED" or "DRAFT").

Hand-delivered responses should be brought to Knox Building, 501 Dulany Street, Alexandria, VA, First Floor (Receptionist).

NGA NGUYEN PRIMARY EXAMINER

January 8, 2007